

Senior Level Image Processing Hardware Engineer

We are looking for an experienced hardware design engineer who will become key contributor to our aggressive development team. The ideal candidate is a person who has hands-on FPGA/ASIC image processing expertise and project management experience.

Arecont Vision is a global market leader in megapixel network cameras and systems for the security industry. Leveraging its extremely cost-efficient massively-parallel MegaVideo® and SurroundVideo® technologies, Arecont Vision supplies world's fastest multi-megapixel (8M, 5M, 3M, 2M and 1.3M) H.264 and MJPEG network cameras at the price points comparable to conventional analog video.

In order to maintain our leading position in the marketplace and to stay on the cutting edge of technology, we place very strong emphasis on in-house development of unique patentable technologies and products. We pay very competitive salaries and revenue-sharing bonuses to allow every key employee to fully share in our success.

Key words: Real-time image processing and algorithms, VHDL, FPGA, high-speed digital design, embedded micro-processor programming, C/C++, RTOS.

Job summary:

- develop new architectures for the next generation of single- and multi-sensor HDTV network cameras for surveillance applications
- improve existing and develop new VHDL modules for single- and multi-sensor HDTV network cameras for surveillance applications
- coordinate interoperability of new digital modules with embedded camera firmware
- help manage long-range strategic product development projects

Education Requirements:

- Ph.D. in EE with 5+ years of experience in the related field, or an equivalent

Required Skills:

- Hardware design: VHDL, simulations, synthesis, FPGA
- Familiarity with high-speed hardware design methodologies
- Proven experience with real-time image processing: compression, dynamic range enhancement, histogram equalization, etc.
- Familiarity with RTOS, C/C++ and embedded micro-controller development